

SUBSTITUTE for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	10/648,055
				Filing Date	August 26, 2003
				First Named Inventor	Chakraborty, Tapan
				Art Unit	2184
				Examiner Name	Wilson, Yolanda
Sheet	1	of	1	Attorney Docket Number	Chakraborty 8-2 [Y0242-00286]

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T
		Country Code - Number - Kind Code (if known)				

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T
YW	A	Nadeau-Dostie, B. et al., "A New Hardware Fault Insertion Scheme for System Diagnostics Verification", Proc. International Test Conference, Oct. 1995, pp. 994-1002	

Examiner Signature	/Yolanda Wilson/	Date Considered	02/21/2007
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.